

Abstract of the Disclosure:

An integrated synchronous memory has a register which can store a frequency-range information item regarding whether the memory is operated in a first or in a lower, second frequency range in an application. The mode of operation of a subcircuit in the memory can be controlled on the basis of the stored frequency-range information item in the register. A memory configuration having a memory module on which at least one such synchronous memory is disposed contains a controller which can be connected to the memory module and sets the register in the at least one memory. Therefore, optimum functionality of the memory can be ensured both in a high and in a low frequency range of the operating frequency.

REL/nt